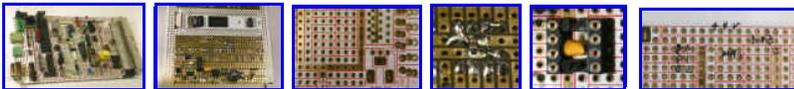


Universal through-hole and SMD prototyping board

I got tired looking for a prototyping board that had all the stuff I wanted, so decided to design my own....

22 Dec 2012 - New MK.3 version now in stock!

- High quality Fibreglass Through-plated PCB for high strength & reliability.
- Standard 100x160mm Eurocard format
- Solder resist reduces risk of solder shorts.
- Gold plated finish for good solderability and long shelf life
- Overall perforated groundplane for good high-speed signal integrity
- 1mm hole size in main TH area allows fitting of 0.1" pin headers etc.
- Underside track connectivity indicated by printed lines on top side silkscreen.
- White finish allows writing on with fine permanent marker.
- Hole grid allows snapping into multiple smaller sections.
- Dense through-hole pattern with groups of 3 interconnected pads for high density DIP layouts.
- Uncommitted power/common clock rails can be linked as required.
- Rectangular pads on underside allow easy use of SM chip parts, as well as allowing quick connection of DIP pins to each other and to power/ground rails with solder-blobs.

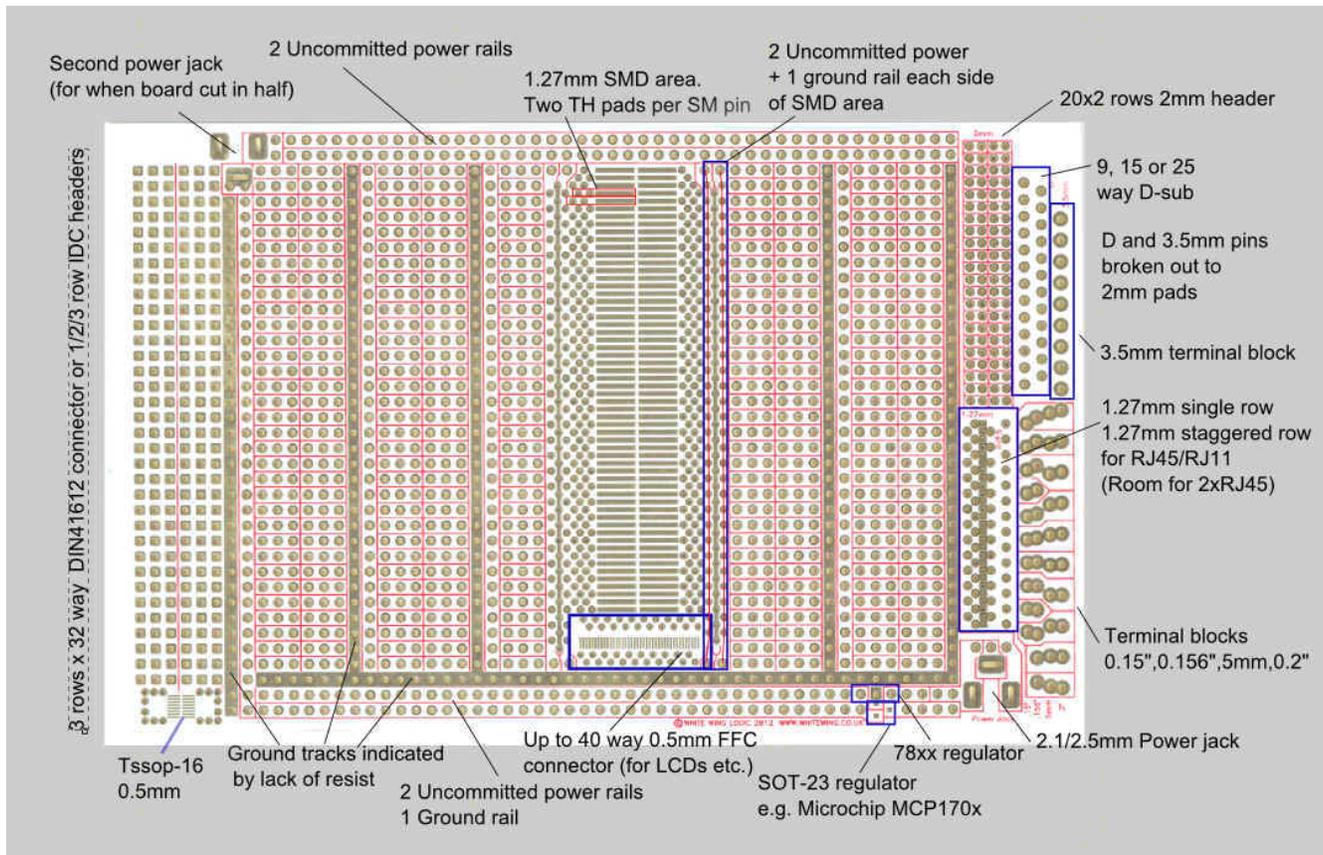


Ordering info

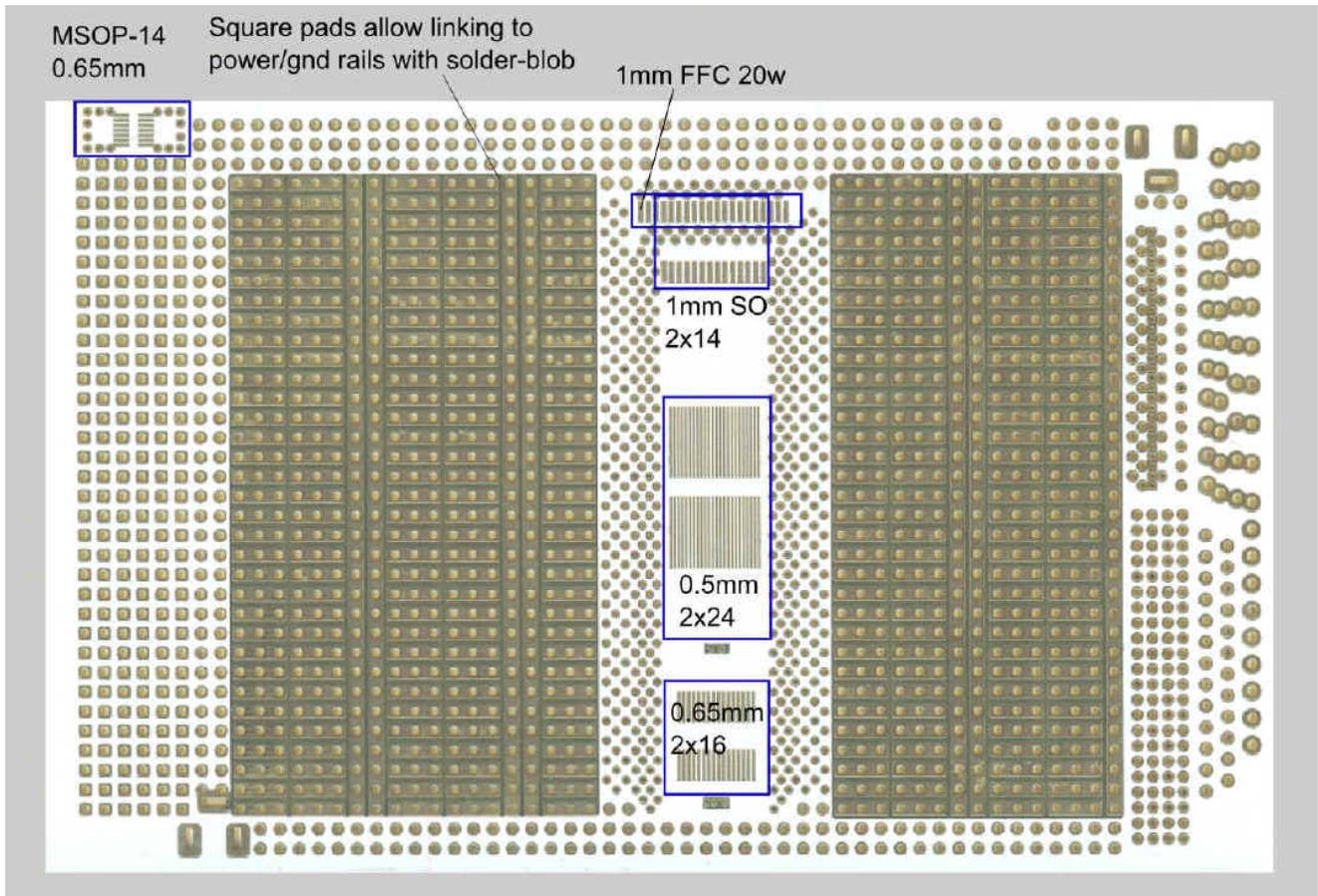
Payment by Paypal only - click on price for Paypal payment link.

Quantity	UK	Europe	Rest of world
2	£16	£18	£19
4	£30	£32	£34

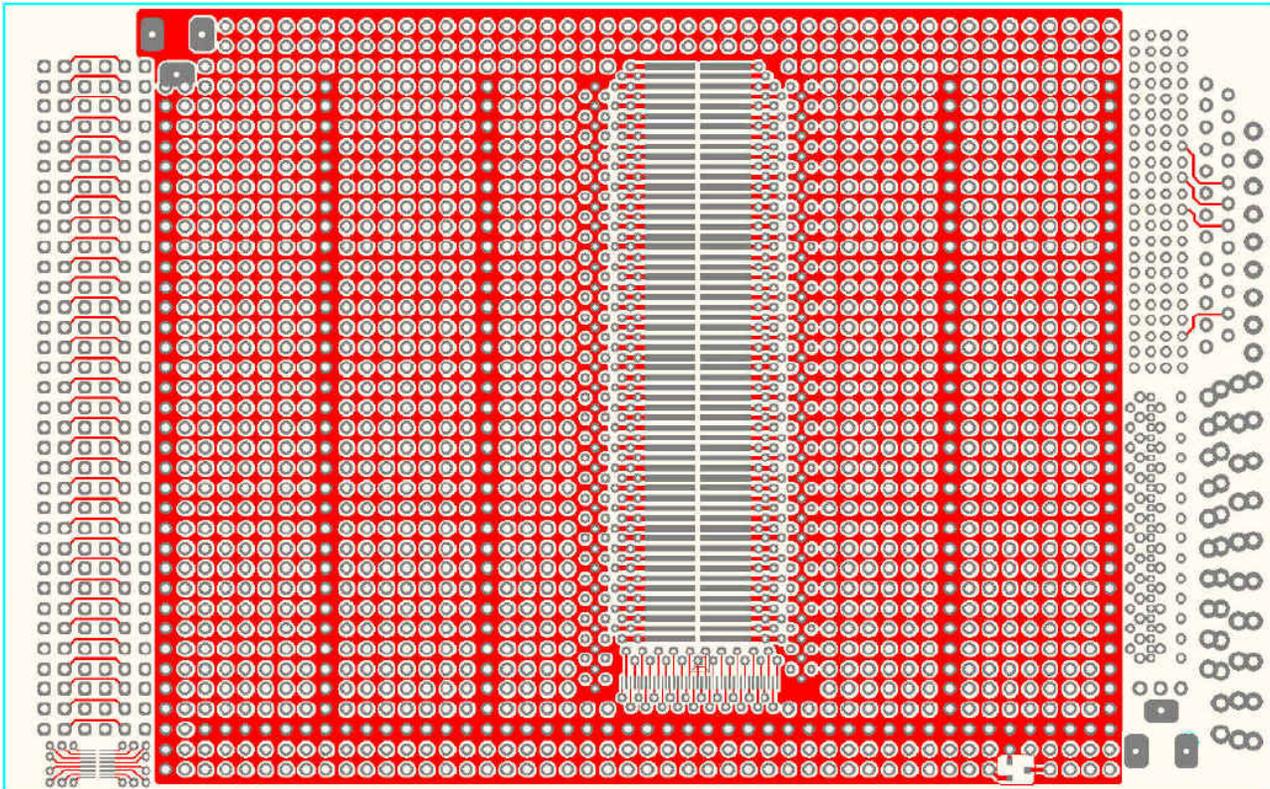
Top side (actual image of board)



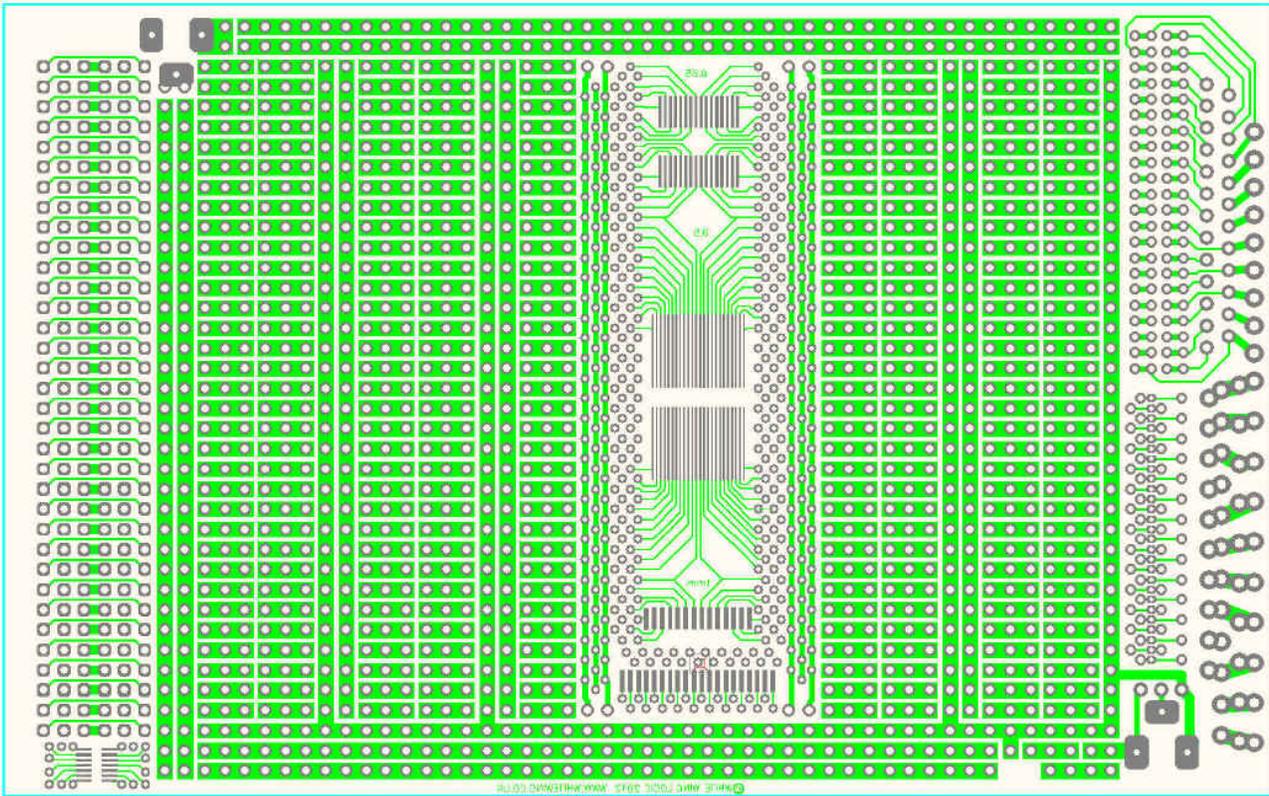
Underside (actual image of board)



Top Copper



Bottom copper (top view)

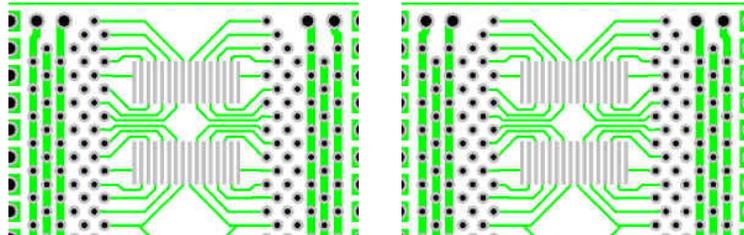


Underside breakout tracking details

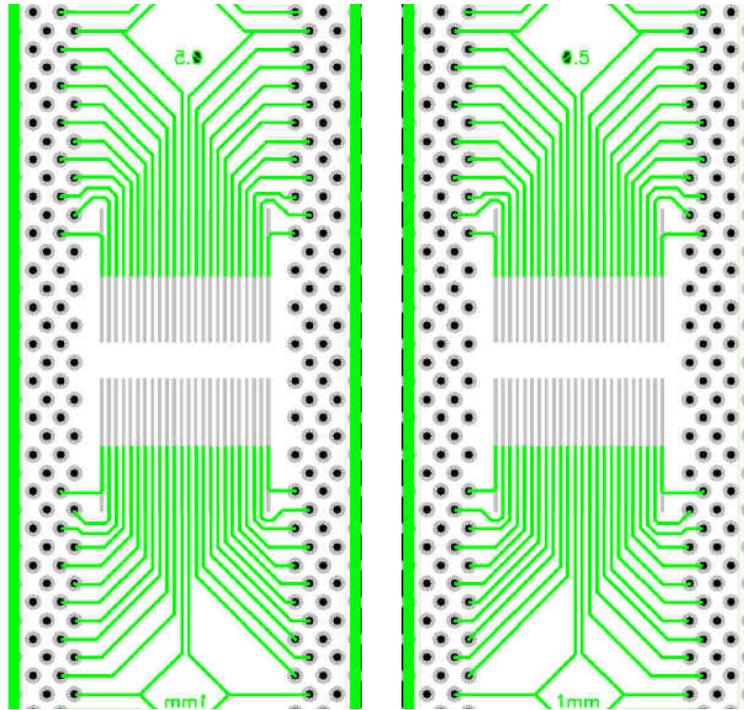
View from top side

View from bottom side

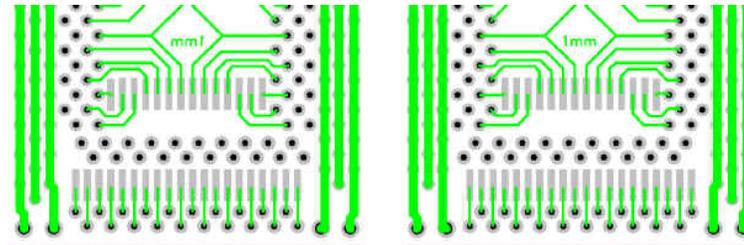
0.65mm
MSOP



0.5mm TSOP



1mm SO/FC



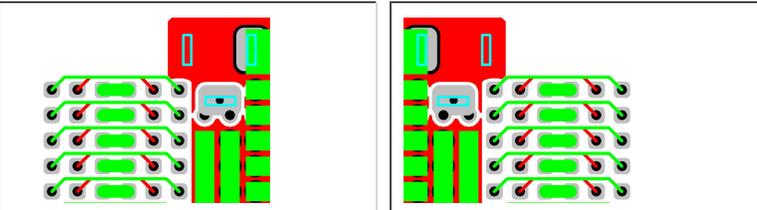
Small
0.65mm
MSOP, bottom

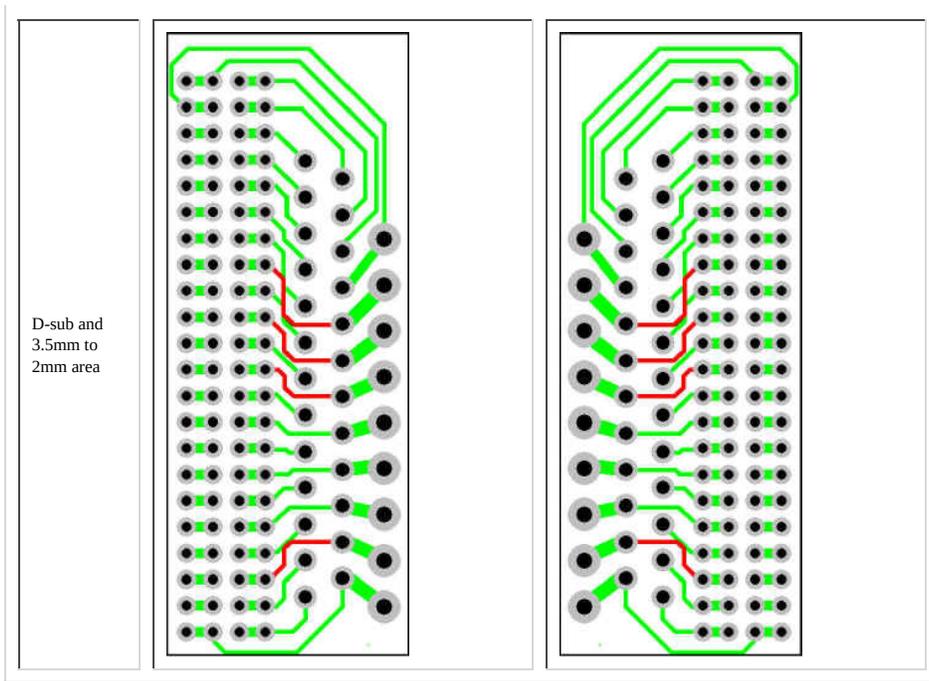


Small 0.5mm
TSOP, top



DIN41612
Red = top
side





Bug list for Mk.3

1) Hole sizes on .156/.2" pads could do with bring bigger

Workaround : Drill them out.

2) Row spacing on small 0.5/0.65mm breakout too small for some packages.

Workaround : Use central breakout area.

3) White resist, although good for marking, too opaque to see tracking clearly - maybe add more silkscreen info next time.